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Docket No. 241473US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Hiroaki HAZAMA

SERIAL NO: New Application

GAU:

FILED:

Herewith

EXAMINER:

FOR:

A SEMICONDUCTOR MEMORY DEVICE HAVING A GATE INSULATION FILM AND A

MANUFACTURING METHOD THEREOF

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- A check is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of
- □ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment form is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Marvin J. Spivak

Registration No. 24,913 C. Irvin McClelland Registration Number 21,124

22850

Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 05/03)

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FILM AND A MANUFACTURING METHOD THEREOF

STATEMENT OF RELEVANCY

Reference AO on Form PTO-1449:

Fabrication method of MOS transistors using the gate oxide contained D2.

Reference AP on Form PTO-1449:

Fabrication method of the semiconductor devices using deuterium to improve the performance.

Reference AQ on Form PTO-1449:

Semiconductor devices using deuterium to improve the performance.

Reference AR on Form PTO-1449:

This shows a method of preventing an occurrence of electron traps.

| Form PTO 1449 (Modified) | | U.S. DEPARTMENT PATENT AND TRA | OF COMMERCE | ATTY DOCKET NO. | | SERIAL NO. | | |
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| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB CLASS | FILING DATE IF APPROPRIATE | |
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| | | NUMBER | | COUNTRY | | YES | | |
| | AO | 11-274489 | 10/08/99 | Japan (with English Abstract) | | | X | |
| - | AP | 11-284183 | 10/15/99 | Japan (with English Abstract) | | | X | |
| | AQ | 2000-77621 | 03/14/00 | Japan (with English Abstract) | | | X | |
| | AR | 2002-118252 | 04/19/02 | Japan (with English Abstract) | | | X | |
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| | | OTHER RE | FERENCES (I | ncluding Author, Title, Date, Pertiner | nt Pages, et | c.) | | |
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